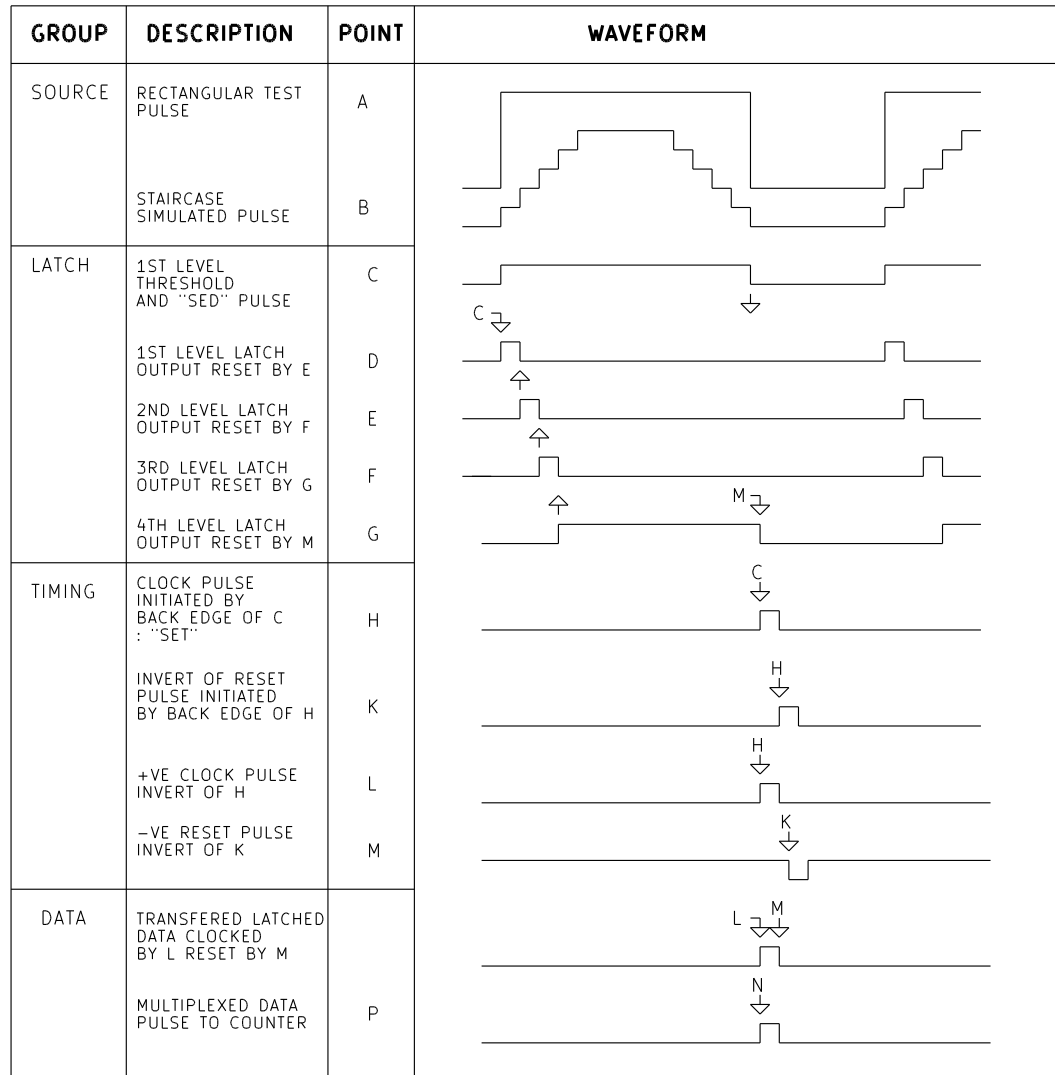


## WAVEFORM DIAGRAM



## NOTES

#	DATE	NOTES
1	20130506	COMPONENT DESIGNATIONS REFLECT AVAILABLE LIBRARY AND THERE MAY BE DIFFERENCES FROM THOSE SHOWN IN COMPONENTS LIST. THE COMPONENTS LIST TAKES PRECEDENCE.
2	20131005	THE SCHEMATIC HAS BEEN REDRAWN IN KICAD (EESCHEMA). THE MDX009 ELECTRONIC ASSYS ARE DIAGRAMMATIC REPRESENTATIONS.
3	20131005	BIASED DIFFERENTIATION IS USED TO TRIGGER U6,U8 AND CAPACITIVE SLUGGING TO LIMIT INPUT SPIKES AT U9. IN EACH CASE TO IMPROVE NOISE IMMUNITY